EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	("5771385").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/10 10:05
L3	470	assert$3 with operating adj system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:27
L4	3531	717/124-135.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:06
L5	10	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:06
L7	6	assert$3 adj3 request adj type	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:22
L10	194	assert$3 adj3 request with (enable or disable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:22
L12	3	4 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:23
L13	11239	(assert$3 or debug$4 or trac$3 or test$3) with operating adj system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:27

L14	680	4 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:27
L17	48093	execut$3 adj process	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:29
L18	118	16 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:29
L19	104	18 and request$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 10:29
L20	2	("5355487").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/10 10:49
L21	2	("6009270").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/10 12:40
L22	307	assert adj active	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 12:41
L24	0	assert adj active adj3 on	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/10 12:41

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Scholar   All articles - Recent articles Results 1 - **10** of about **40,600** for request enable OR disable

All Results

V Pai

M Aron

G Banga

M Svendsen

P Druschel

Method of prechecking the validity of a write access request - all 3 versions »
G Baror, M Beck, D Biran, E Cohen, Y Hadas, B ... - US Patent 5,438,670, 1995 - Google Patents
... [45] Date of Patent: [54] METHOD OF PRECHECKING THE VALIDITY OF A WRITE ACCESS **REQUEST**
[75] Inventors ... Bus Access Control Exception **Request** Internal Status ...
Cited by 19 - Related Articles - Web Search

Setting and getting system debug flags by name at runtime - all 3 versions »
JM Harper... - US Patent 5,771,385, 1998 - Google Patents
... function contained therein (ie, **disable/ enable** file caching ... Based on the user's **request**, operation 74 builds an appropriate **debug request** debugging message ...
Cited by 17 - Related Articles - Web Search

System for dynamically generating, correlating and reading multiprocessing trace data in a shared ... - all 3 versions »
GR Alderson - US Patent 5,347,649, 1994 - Google Patents
... and a flag may be set to **disable** further tracing ... **enable** said **trace** function to read said **trace** data from ... are used to **request** the **trace** function to display ...
Cited by 25 - Related Articles - Web Search

Apparatus and method in a network interface for enabling power up of a host computer using magic ... - all 3 versions »
RA Williams, JR Dwork... - US Patent 5,938,771, 1999 - Google Patents
... override circuit for causing **assertion** of the power manage -ment **enable** input in response to a supplied **enable** override bit and the detected wake up **request**. ...
Cited by 35 - Related Articles - Web Search

Locality-aware request distribution in cluster-based network servers - all 16 versions »
VS Pai, M Aron, G Banga, M Svendsen, P Druschel, W ... - ACM SIGPLAN Notices, 1998 - portal.acm.org
... 0 0.2 0.4 0.6 0.8 1 Files by **request** frequency (normalized) Figure 5: Rice [Jniversity **Trace** of **request**#s in the **trace** that were served per second by the ...
Cited by 490 - Related Articles - Web Search

A Standard Interface for Debugger Access to Message Queue Information in MPI - all 12 versions »
J Cownie, W Gropp - Recent Advances in Parallel Virtual Machine and Message ..., 1999 - books.google.com
... routines) removes the **request** from the list. This extra list is conditionally compiled into MPICH when MPICH is configured with the—**enable-debug** switch. ...
Cited by 51 - Related Articles - Web Search

Data processing system including a separate input/output processor with micro-interrupt request ... - all 2 versions »
JM Guyer, JT West - US Patent 4,296,466, 1981 - Google Patents

... translating logical addresses received from the **request**- ... QUEST **ENABLE** signal (HRQENB)
in accordance ... to review the operation ofa known 40 **disable** the comparator ...
Cited by 22 - Related Articles - Web Search

System and method for extracting realtime **debug** signals from an integrated circuit - all 3 versions »
PG Tobin - US Patent 5,838,692, 1998 - Google Patents
... may be used to obtain a realtime **trace** of internal ... DB F/B. The **debug** I/O bus **request**
signal DB ... main system CPU clock edge CPU_CK if the **debug enable** signal DB ...
Cited by 21 - Related Articles - Web Search

Memory management unit for the MIL-STD 1750 bus - all 2 versions »
HG Hackbarth - US Patent 5,079,737, 1992 - Google Patents
... **disable** the bus **request** feature for one or both ... by said CPU through said bus **request**
enable pins to ... discriminates between bus masters by **assertion** of said bus ...
Cited by 45 - Related Articles - Web Search

Bus master capable of relinquishing bus on **request** and retrying bus cycle - all 2 versions »
WP LaViolette, DS Mothersole, J Zolnowsky - US Patent 4,602,327, 1986 - Google Patents
... In response to the **assertion** ofOWN, channel controller 22 ... thereafter assert a LATCH
signal to **enable** cycle type ... assert a START signal to **request** bus controller ...
Cited by 24 - Related Articles - Web Search

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request enable OR disable assertion   Search

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